CLAIMS

1.   A method for transmitting digital messages through output terminals (22) of a microprocessor supervision circuit (18) of a determined type from among several supervision circuit types integrated to a microprocessor (12), each message comprising a message identifier and being formed of several

5   groups of successive juxtaposed bits, the bit groups being divided in one or several segments each comprising a determined number of bits, the method consisting of successively transmitting segments associated with the following successive juxtaposed bit groups:

a first bit group corresponding to the identifier and comprising a fixed

10   number of bits whatever the supervision circuit type;

second bit groups, at least one of the second groups comprising a fixed number of bits depending on the identifier and on the type of supervision circuit, the number of the other second groups depending on the identifier and being independent from the supervision circuit type;

15   a third bit group comprising a number of bits greater than one and depending on the message to be transmitted; and

fourth bit groups each comprising a number of bits greater than one and depending on the message to be transmitted, the number of fourth groups depending on the identifier, on the supervision circuit type, and on the message

20   to be transmitted.

2.   The method of claim 1, in which said at least one of the second groups is juxtaposed to the first group.

25   3.   The method of claim 1, in which each of said other second groups has a number of bits which depends on the identifier and which is independent from the type of supervision circuit.

4.   The method of claim 1, in which each of said other second groups has a number of bits greater than one which depends on the message to be transmitted.

5.   5.   A device of digital message transmission through output terminals (22) of a supervision circuit (18), of a determined type from among several supervision circuit types, integrated to a microprocessor (12), each message comprising a message identifier, said device comprising means for providing groups of successive juxtaposed bits forming the message, means for dividing

10   the bit groups in one or several segments each comprising a determined number of bits and means for successively transmitting said segments, the bit group provision means being capable of successively providing a first bit group corresponding to the identifier and comprising a fixed number of bits identical whatever the supervision circuit type, second bit groups, at least one of said

15   second groups comprising a fixed number of bits depending on the identifier and on the type of supervision circuit, the number of the other second groups depending on the identifier and being independent from the supervision circuit type, a third bit group comprising a number of bits greater than one and depending on the message to be transmitted, and fourth bit groups each

20   comprising a number of bits greater than one and depending on the message to be transmitted, the number of fourth groups depending on the identifier and on the determined supervision circuit type.